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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
*NONE SBG*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
JAPAN P2002-369422 12/20/2002 *SBG*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>Law D. G.</i>	INITIALS <i>SBG</i>		

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TITLE  
Fabrication process of a semiconductor integrated circuit device

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